## Rajasthan Technical University, Kota Teaching & Scheme of Examination for B.Tech. Computer Science w.e.f. session 2010-11

5CS2 V	V V V	Computer Architecture  Digital Logic Design  Telecommunication  Fundamentals	3	3	1		С				Common to CS,
	V	Telecommunication	3					20	80	100	IT
5CS3 V				3	-		С	20	80	100	
	\/	i diladilicilale	3	3	-	-	С	20	80	100	Common to CS, IT
5CS4 V	V	Database Management Systems	3	3			С	20	80	100	Common to CS, IT
5CS5 V	V	Operating Systems	3	3	-		С	20	80	100	Common to CS, IT
5CS6.1 V	V	Advanced Data Structure	3	3	-		Е	20	80	100	Common to CS, IT
5CS6.2 V	V	Digital Signal Processing	3	3			Е	20	80	100	
5CS6.3 V	V	Information Theory & Coding	3	3			Е	20	80	100	
5CS7 V	V	DataBase Lab	4			3		60	40	100	Common to CS, IT
5CS8 V	V	System Design in UML Lab.	4	-	-	3		45	30	75	
5CS9 V	V	Operating Systems Simulation Lab	4			3		60	40	100	Common to CS, IT
5CS10 V	V	Digital Hardware Design Lab	4			3		45	30	75	Common to CS, IT
5CSDC V	V	Discipline & Extra Curricular Activities								50	

TOTAL 18 1 12 1000

(S.S. Agarwal)

Member

wal) (C.P. Gupta)

Member Secretary (Prof. M.C. Govil)

Chairman

6CS1	VI	Computer Networks	3	3	-		С	20	80	100	Common to CS,
6CS2	VI	Design and Analysis of Algorithms	3	3	-		С	20	80	100	Common to CS,
6CS3	VI	Theory Of Computation	3	3	1		С	20	80	100	Common to CS,
6CS4	VI	Programming in Java	3	3			С	20	80	100	Common to CS,
6CS5	VI	Embedded System Design	3	3	-		С	20	80	100	
6CS6.1	VI	Advance Topics in Operating Systems	3	3	-		Е	20	80	100	Common to CS,
6CS6.2	VI	Artificial Intelligence	3	3			Е	20	80	100	
6CS6.3	VI	Human Computer Interface	3	3			Е	20	80	100	Common to CS,
6CS7	VI	Java Programming Lab	4			3		60	40	100	Common to CS,
6CS8	VI	Shell Programming Lab.	4			2		30	20	50	Common to CS,
6CS9	VI	Design and Analysis of Algorithms Lab.	4			3		60	40	100	Common to CS,
6CS10	VI	Embedded System Design Lab.	4			3		60	40	100	
6CSDC	VI	Discipline & Extra Curricular Activities								50	

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