

# Rajasthan Technical University Akelgarh, Rawatbhata Road, Kota-324010

### **INVITATION LETTER**

Package Code: TEQIP-III/RJ/rtur/31

Current Date: 29-Jun-2019

Package Name: Wind & Solar Emulators

**Method: Shopping Goods** 

To,

Sub: INVITATION LETTER FOR Wind & Solar Emulators

Dear Sir,

Sr. No	Item Name	em Name Quantity Place of Delivery		Installation Requirement (if any)	
1	Grid tied Wind Energy Emulator Module	Energy Emulator	Coordinator, TEQIP-III RTU (ATU) Office of TEQIP-III, First Floor, VC Secretariat, Rawatbhata Road, Akelgarh, Rajasthan Technical University, Kota- 324010 (Raj.)	YES	
2	Grid tied Solar Module	1	Coordinator, TEQIP-III RTU (ATU) Office of TEQIP-III, First Floor, VC Secretariat, Rawatbhata Road, Akelgarh, Rajasthan Technical University, Kota- 324010 (Raj.)	YES	

2.	towards Project a	nent of India has received a credit from the International Development Association (IDA) the cost of the <b>Technical Education Quality Improvement Programme [TEQIP]-Phase III</b> and intends to apply part of the proceeds of this credit to eligible payments under the contract this invitation for quotations is issued.				
3.	Quotation					
	3.1	The contract shall be for the full quantity as described above.				
	3.2	Corrections, if any, shall be made by crossing out, initialling, dating and re writing.				
	3.3	All duties and other levies payable by the supplier under the contract shall be included in the unit price				
	3.4	Applicable taxes shall be quoted separately for all items.				
	3.5	The prices quoted by the bidder shall be fixed for the duration of the contract and shall not be subject to adjustment on any account.				
	3.6	The Prices should be quoted in Indian Rupees only.				

4.	Each bidder shall submit only one quotation.
5.	Quotation shall remain valid for a period not less than 60days after the last date of quotation submission.
6.	Evaluation of Quotations: The Purchaser will evaluate and compare the quotations determined to b substantially responsive i.e. which
	6.1 are properly signed; and
	6.2 Confirm to the terms and conditions, and specifications.
7.	The Quotations would be evaluated for all items together
8.	Award of contract: The Purchaser will award the contract to the bidder whose quotation has bee determined to be substantially responsive and who has offered the lowest evaluated quotation price
	8.1 Notwithstanding the above, the Purchaser reserves the right to accept or reject any quotations and to cancel the bidding process and reject all quotations at any time prior to the award of contract.
	8.2 The bidder whose bid is accepted will be notified of the award of contract by the Purchaser prior to expiration of the quotation validity period. The terms of the accepted offer shall be incorporated in the purchase order.
9.	Payment shall be made in Indian Rupees as follows: Satisfactory Delivery & Installation - 10% of total cost (September 28, 2019) Satisfactory Acceptance - 90% of total cost (September 28, 2019)
10.	Liquidated Damages will be applied:  Liquidated Damages Per Day Min %: 0.1  Liquidated Damages Max %: 10
11.	All supplied items are under warranty of 12 months from the date of successful acceptance of items and AMC/Others is <b>Not Required.</b> .
12.	The items covered under this invitation are required to be delivered & installed at RTU Kota within 60 days from the date of issue of Purchase Order.
13.	The sealed bid, complete in all respects, must reach, Coordinator, TEQIP-III, RTU (ATU), V Secretariat, Rajasthan Technical University, Rawatbhata Road, Akelgarh, Kota-324010 latest 19 July 2019 up to 16:00 hrs., failing which it would be summarily rejected. RTU will not be responsible for postal delay or non-receipt of quotation.
14.	The quotation would be opened on 19 July 2019 at 16:00 hrs. at Coordinator, TEQIP-III, RT (ATU), VC Secretariat, Rajasthan Technical University, Rawathhata Road, Akelgarh, Kot 324010 in the presence of bidder representatives who choose to attend the opening. The bidd representatives who are present shall sign an Attendance sheet evidencing their attendance.
15.	Bidder must quote the Financial Rate strictly as per financial Quotation format provided at <b>Annexur</b> IV.
16.	Detailed specifications of the items are at Annexure-I.
17.	Training Clause (if any) YES
18.	The bidders must provide a certificate indicating their adherence to all the clauses of the bid as p format in Annexure-II
19.	The bidders must provide a Technical Compliance Report per format in Annexure-III
20.	Testing/Installation Clause (if any) YES

	15 days from the date of issue of purchase order.
22.	To ensure the price justification of the above proprietary item, bidder shall attach copies of the similar nature of recent purchase orders (minimum two) preferably issued by the government /semi government institutions / autonomous bodies.
23.	Information brochures/ Product catalogue, if any must be accompanied with the quotation clearly Indicating the model quoted for
24.	We look forward to receiving your quotation and thank you for your interest in this project.

(Authorized Signatory)

Name & Designation

Coordinator, TEQIP-III (ATU)
Rajasthan Technical University

Dhizandra Mathre

Kota (Rajasthan)

## Annexure- I

Sr. No	Item Name	Specifications			
1	Grid tied Wind	Wind Emulator Machine Details: 1 Qty			
	Energy	220V, 2.5KW, 1500 rpm DC shunt Motor Coupled to 415V, 2.2 KW			
. 1	Emulator Module	1000 rpm 6 pole Three phase slip ring induction motor, 1024 ppr encoder included.			
		Along with required power supplies for Sensors and shielded cables			
		for motor terminals			
		Three phase IGBT based Converter details (Rectifier + Inverter + Brake Chopper): 3 Qty			
		I/P AC Voltage: - 415 Volt			
		DC Voltage: - 600 Volt			
		O/P AC Voltage: - 415 Volt			
		O/P AC Current 30 Amp			
		Switching Frequency upto 20 kHz			
		Fundamental Frequency 50 Hz			
		Type of Cooling: - Forced Air			
		Ambient Temp: - 40 Deg			
		Duty Class: - Class I			
		Cooling Method Forced Air Cooled.			
		IGBT Details			
		V-IGBT=6. Generation Trench V-IGBT			
		CAL4= Soft switching 4 Generation CAL-diode.			
		Isolated copper baseplate using DCB technology (Direct Copper			
		Bonding).			
		Increased power cycling capability.			
		With integrated gate resistor.			
		Low switching losses at high di/dt.			
		Bridge Module			
		Three phase bridge rectifier			
		Blocking voltage of 1600 V			
		High surge current carrying capability			
		Large isolated base plate & Easy mounting			
		Gate Driver			
		It should interface and isolate the Control Unit/Primary Circuit from			
	the secondary which is directly connected to the high power.				
		Gate Driver controls the IGBT's dynamic behavior and its short circuit protection.			
		Input signal level is 0/15V.			
		Interlocking time between the input signals is 3 µs.			
		It monitors the errors: power supply under-voltage (below 13.3 V),			

am

short-circuit between Collector and Emitter. The error reset time is typically 9µs.

On detection of error/fault, the Gate Driver switches off the IGBT. The IGBT switching speed may be adjusted by the resistors RGON and RGOFF.

The two parameters (Rce, Cce) define the values and time delays for the comparison of an internal reference with the monitored value of Vce(sat).

### Kit Details

3-phase teaching kit consists of 3-phase uncontrolled rectifier and 3-phase IGBT based controlled Inverter. It should also have a brake chopper.

3-phase 415 V input is applied to the uncontrolled rectifier (SKD100/16) using an autotransformer

The dc output of the Rectifier is given to the input of the IGBT based inverter.

DC capacitor bank is connected in between the rectifier and inverter as source to the inverter.

3 phase 415 V ac Output is achieved from the inverter and is provided for the Motor Interface.

Driver is the interface unit between the power module and controller. Each Driver drives 2 switches in a Module.

+15V/0V supply is given to Vs and GND. Alternate ON/OFF pulses of +15V are given to Vin1 and Vin2.

Vin1 corresponds to TOP IGBT and Vin2 corresponds to BOTTOM IGBT.

ERROR is triggered when Vs falls below 13.3 V and Short circuit of IGRT

ERROR output is taken to the controller for turning off the system during occurrence of the error.

### Capacitor & Snubber

Rectified DC input is given to electrolytic filtering capacitors. Each capacitor is 4700  $\mu F$  / 450 V.

2 capacitors are connected in series to have equivalent capacitance of 2350  $\mu F$  / 900  $V_{\cdot}$ 

Resistors of value  $27\ k\,/\,20\ W$  are connected across each capacitor for voltage balancing.

Snubber Capacitors of 0.22  $\mu F$  / 1500 Vdc (4 nos) are connected across the dc link for voltage overshoot protection.

The snubbers limit the over-voltages during commutations and as a consequence reduce the losses.

They are kept very close to the device to reduce the inductance between the switches and the capacitors.

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### Cooling

Teaching kit stack assembly is provided with forced air cooling. IGBT modules are mounted on 250 mm heat sink (extruded type). Forced air-cooling is provided by Hi Cool Fan. 230VAC 1ph. should be applied to the Fan input terminals provided on the unit. Flow of air is 3 m/s.

Input to the fan is  $1-\Phi$  230 V Supply

#### **Thermal Protection**

Normally Closed Thermal contact switch is used for protection against thermal runaway.

The position of the thermal switch normally closed when its temperature is below the threshold temperature (80 deg C) & it changes to normally open above 80 deg C.

After cooling down, it again retains it normally closed position. Thermal switch is placed at the warmest point on the heat sink

### Wavect Controller: 1 Qtv

It is having a Xilinx FPGA Zynq<sup>TM</sup>-7000 SoC XC7Z020-CLG484-1with Dual ARM® Cortex<sup>TM</sup>-A9 MPCore<sup>TM</sup>

Which have capability Up to 667 MHz operation along with NEON<sup>TM</sup> Processing / FPU Engines.

Memory allocation in the board is given by 512 MB DDR3 and 256 Mb Quad-SPI Flash with Full size

Isolated Voltage (8) and current (8) sensors for feedback Voltage +/- 0-1000V AC/DC measurement and output is instantaneous.

Current Sensor 0-25 AC/DC measurement and output is instantaneous. Dual, 200KSPS-1MSPS 16 bit 4x2 channel Simultaneous sampling for ADC for sensors alone.

PWM Card 3.3/5 to 15 V Level Shifting of 12x2 PWM Out with optical isolation.

Dedicated I/O with for encoder and Hall sensors with 5V supply and Differential noise reduction.

4 Digital Input which can take 3.3V/5V input to FPGA. 230V, 5A, 4 numbers of NO-NC Contactor Relay for HV Driving purpose.

### Wavect Software: 1 Qty

Direct link to Matlab Simulink software. Capability to make the algorithm schematically in Simulink environment using the System generator tool box in Simulink and transfer the code to the controller for hardware implementation. Sensor outputs are available as schematic blocks making ADC and sensor interfacing simplified. Building blocks for the wind energy system like the GSC (grid side converter), RSC (Rotor side converter), PLL (Phased locked loop) etc

will be provided with the module. These blocks are open and editable.

Grid Side Reactor, Contactors, Capacitor: 1 set 3Phase Line reactor: 440V, 25A, 10mH,50/60Hz

Power Capacitor: 440V, 2kVAR, 2.6A 3Phase Contactor: 440V, 50A, 50/60Hz L & C Filters, Switches, Relays: 1 set

Required Numbers of L, C filters for designed ratting. Switches and relays along with relay drives for the protection is also provided with system.

Integration With WAVECT and Software + Installation + Onsite Training + 1 year Warranty

### 2 Grid tied Solar Module

### Three phase IGBT based Inverter details: 1 Qty

I/P AC Voltage: - 415 Volt

DC Voltage: - 600 Volt

O/P AC Voltage: - 415 Volt

O/P AC Current 30 Amp

Switching Frequency 20 kHz

Fundamental Frequency 50 Hz

Type of Cooling: - Forced Air

Ambient Temp: - 40 Deg

Duty Class: - Class I

Cooling Method Forced Air Cooled.

### **Boost Converter Specifications**

I/P DC Voltage: - 200 Volt

O/P DC Voltage: - 600 Volt

O/P DC Current upto 10 Amps

Switching Frequency upto 20 kHz

Fundamental Frequency 50 Hz

Type of Cooling: - Forced Air

Ambient Temp: - 40 Deg

Duty Class: - Class I

Cooling Method Forced Air Cooled.

# Common Specification for Inverter and Boost Converter IGBT Details

V-IGBT=6. Generation Trench V-IGBT

CAL4= Soft switching 4 Generation CAL-diode.

Isolated copper baseplate using DCB technology (Direct Copper

Bonding).

Increased power cycling capability.

With integrated gate resistor.

Low switching losses at high di/dt.

**Gate Driver** 

It should interface and isolate the Control Unit/Primary Circuit from the secondary which is directly connected to the high power.

Gate Driver controls the IGBT's dynamic behaviour and its short circuit protection.

Input signal level is 0/15V.

Interlocking time between the input signals is 3µs.

It monitors the errors: power supply under-voltage (below 13.3 V), short-circuit between Collector and Emitter. The error reset time is typically 9µs.

On detection of error/fault, the Gate Driver switches off the IGBT. The IGBT switching speed may be adjusted by the resistors RGON and RGOFF.

The two parameters (Rce, Cce) define the values and time delays for the comparison of an internal reference with the monitored value of Vce(sat).

### **Kit Details**

The dc output is given to the input of the IGBT based inverter.

DC capacitor bank is connected in between the rectifier and inverter as source to the inverter.

3 phase 415 V ac Output is achieved from the inverter and is provided for the Motor Interface.

Driver is the interface unit between the power module and controller. Each Driver drives 2 switches in a Module.

+15V/0V supply is given to Vs and GND. Alternate ON/OFF pulses of +15V are given to Vin1 and Vin2.

Vin1 corresponds to TOP IGBT and Vin2 corresponds to BOTTOM IGBT.

ERROR is triggered when Vs falls below 13.3 V and Short circuit of IGBT.

ERROR output is taken to the controller for turning off the system during occurrence of the error.

### Capacitor & Snubber

Rectified DC input is given to electrolytic filtering capacitors.

Each capacitor is 4700 µF / 450 V.

2 capacitors are connected in series to have equivalent capacitance of 2350  $\mu F$  / 900 V.

Resistors of value 27 k / 20 W are connected across each capacitor forvoltage balancing.

Snubber Capacitors of 0.22  $\mu F$  / 1500 Vdc (4 nos) are connected acrossthe dc link for voltage overshoot protection.

The snubbers limit the over-voltages during commutations and as aconsequence reduce the losses.

They are kept very close to the device to reduce the inductance

betweenthe switches and the capacitors.

### Cooling

Teaching kit stack assembly is provided with forced air cooling. IGBT modules are mounted on 250 mm heat sink (extruded type). Forced air-cooling is provided by Hi Cool Fan. 230VAC 1ph. should be applied to the Fan input terminals provided on the unit. Flow of air is 3 m/s.

Input to the fan is  $1-\Phi$  230 V Supply

#### **Thermal Protection**

Normally Closed Thermal contact switch is used for protection against thermal runaway.

The position of the thermal switch normally closed when its temperature is below the threshold temperature (80 deg C) & it changes to normally open above 80 deg C.

After cooling down, it again retains it normally closed position. Thermal switch is placed at the warmest point on the heat sink **Wavect Controller: 1 Qty** 

It is having a Xilinx FPGA Zynq<sup>TM</sup>-7000 SoC XC7Z020-CLG484-1with Dual ARM® Cortex<sup>TM</sup>-A9 MPCore<sup>TM</sup>

Which have capability Up to 667 MHz operation along with NEON™ Processing / FPU Engines.

Memory allocation in the board is given by 512 MB DDR3 and 256 Mb Quad-SPI Flash with Full size

Isolated **Voltage (4) and current (4)** sensors for feedback Voltage Sensor +/- **0-1000V** AC/DC measurement and output is instantaneous

Current Sensor **0-25A** AC/DC measurement and output is instantaneous

Dual, 200 KSPS-1MSPS 16 bit 4x2 channel Simultaneous sampling for ADC

15 V, 12 PWM Out.

4 Digital Input which can take 3.3V/5V input to FPGA. 230V, 5A, 4 numbers of NO-NC Contactor Relay for HV Driving purpose.

### Wavect Software: 1 user

Real time capturing Real time capturing and plot up to 32 Channels in 200 ksps rate with buffering option up to 60sec, that includes voltage (8) & current (8) analogue signals and additional add-on card's data along with viewing probes for application algorithm using which logic can be probed.

Up to 32 virtual input/output for control monitoring of digital controller signal.

Real time storage/buffer storage of data for post analysis and data

exporting provision to .png/CSV file format.

Gigabit Ethernet communication with the board for programming, controlling, monitoring and capturing at high data R/W rate.

Real time display of power vector diagram with detailed running statistics.

FFT analysis of the Voltage and current waveform in both Scope mode and programed mode.

Scope mode with voltage and current measuring. Individual panel for solar and drive.

Dashboard arrangement for experiment setups.

Tcl command support for writing values to a file.

Xilinx System generator, Entuple library modules in Matlab for control applications.

Matlab integration for captured data analysis.

Note:

Direct link to Matlab Simulink software. Capability to make the algorithm schematically in Simulink environment using the System generator tool box in Simulink and transfer the code to the controller for hardware implementation. Sensor outputs are available as schematic blocks making ADC and sensor interfacing simplified.

Solar Panel: 1 Qty

The panel will be provided with suitable mounting arrangement along with 30m of cabling. The power rating of the panel is 2500W.

Grid Side Reactor, Contactors, Capacitor: 1 Set

3Phase Line reactor: 440V, 25A, 10mH, 50/60Hz

Power Capacitor: 440V, 2kVAR,2.6A 3Phase Contactor: 440V, 50A, 50/60Hz

L & C Filters, Switches, Relays:

Required Numbers of L, C filters for designed ratting. Switches and relays along with relay drives for the protection is also provided with system.

Integration With WAVECT and Software + Installation + Onsite Training + 1 year Warranty

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# COMPLIANCE STATEMENT FOR VARIOUS CLAUSES IN BID

Package	No
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S. No.	Clause mentioned in Invitation letter	Please mention Agreed /Not Agreed	Remarks
1.	Payment Terms		
2.	Delivery Period – 60 days		
3.	Warranty – 12 Months		
4.	Bid Validity – 60 days		
5.	Training		
6.	Testing/ Commissioning/ Installation included		

Signature	of Supp	lier
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Name:	
Address:	
Contact No:	

## TECHNICAL COMPLIANCE STATEMENT

Package No.----

S. No.	Technical Specification of the equipment asked in the bid	Technical Specification of the equipment offered by the bidder with Model No.	Remarks
1.	As per detailed specifications in Annexure I		

Note: Bidder must give the Model No. of each furniture items quoted along - with original literature

Signature of S	supplie	I
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Name: \_\_\_\_\_\_

Address: \_\_\_\_\_\_

Contact No: \_\_\_\_\_

# FORMAT FOR QUOTATION SUBMISSION (In letterhead of the supplier with seal)

Date	o:								
SI. No.	Description of goods \ (with full Specifications)	Qty.	Unit	Unit rate in Rs. (Including Ex Factory price, excise duty, packing and forwarding, transportation, insurance, other local costs incidental to delivery and warranty/ guaranty commitments)	Total Price (Without taxes) (A)	GST %	GST Amount Rs.	Total Price (Including GST) Rs.	Make & Model No. quoted
1.	As per detailed specifications in <b>Annexure I</b>	01	No.						
				Total Cost					

Total Price (exclusive of taxes) ( in Words)		
Total Price of Bid (Inclusive of taxes) (In Words)		
We agree to supply the above goods in accordance with the technical (Amount in figures) (Rupees ———————————————————————————————————	specifications for a total contract price of Rs. ———————————————————————————————————	

We confirm that the normal commercial warranty/ guarantee of <u>12 months</u> shall apply to the offered items and we also confirm to agree with terms and conditions as mentioned in the Invitation Letter.

We hereby certify that we have taken steps to ensure that no person acting for us or on our behalf will engage in bribery.

Note:	
i)	Discount or any other offers affecting the package price must be mentioned here only. Discount or any other offers affecting the package price mentioned at any other place of the bid will not be considered.
ii)	In case of discrepancy between unit price and total price, the unit price shall prevail.
iii)	Bids shall be evaluated based on total price.

### Signature of Supplier

Name: \_\_\_\_\_

Address: \_\_\_\_\_

Contact No: